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FIG.1

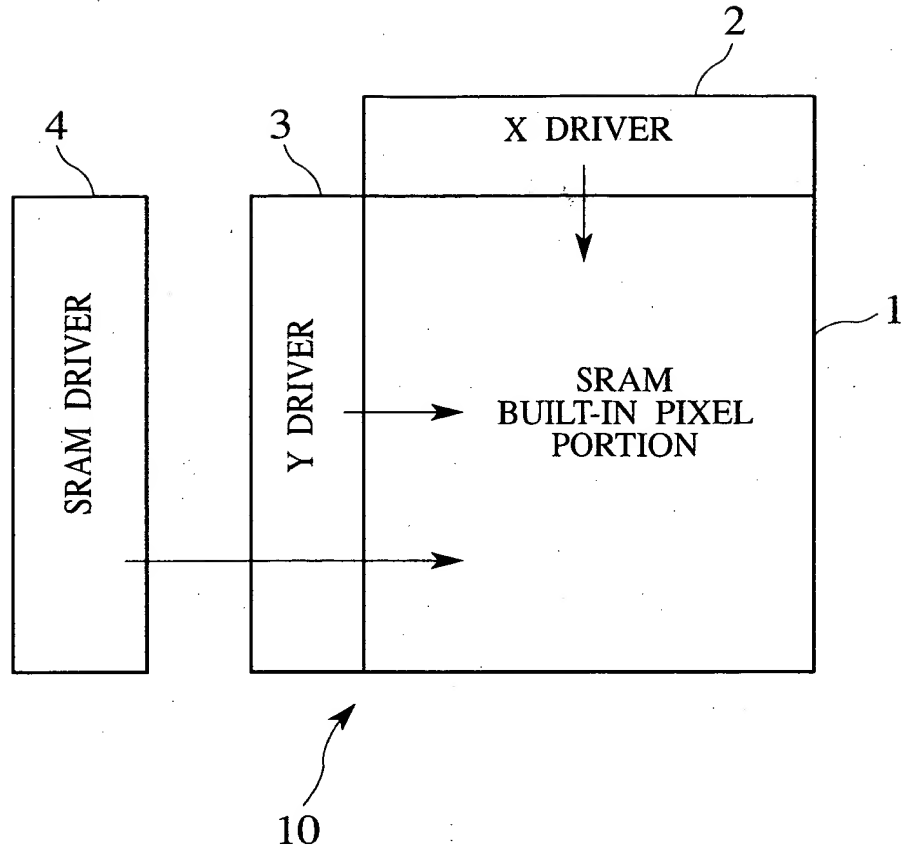


FIG.2

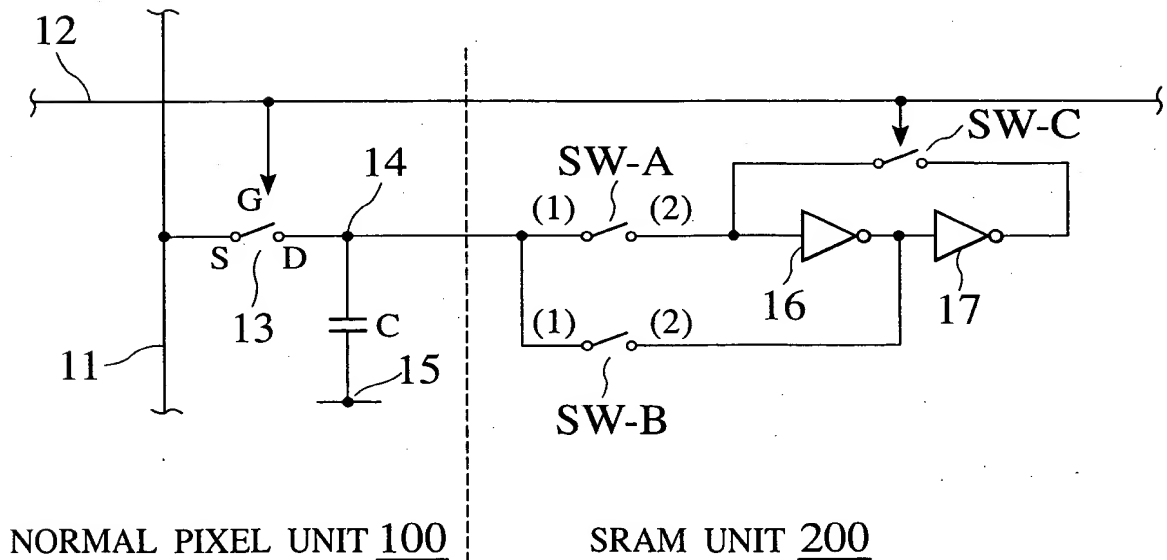


FIG. 3

	NORMAL DISPLAY MODE	WRITE MODE	SRAM DISPLAY MODE							
<u>DURING BLACK DISPLAY</u>										
PIXEL ELECTRODE	ANALOG POTENTIAL		H	L						
OPPOSITE ELECTRODE			H	L						
<u>DURING WHITE DISPLAY</u>										
PIXEL ELECTRODE	ANALOG POTENTIAL		H	L						
OPPOSITE ELECTRODE			H	L						
PIXEL TFT	on/off	on/off			off	off	off	off	off	off
SW-A	off	on			off	off	on	on	on	on
SW-B	off	off			on	on	off	off	on	on
SW-C	off/on	off/on			on	on	on	on	on	on

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FIG.4

CIRCUIT CONFIGURATION	POWER SOURCE VOLTAGE		SRAM DRIVE
	VDD	VSS	
• X DRIVER SHIFT REGISTER DATA LATCH GRADATION VOLTAGE SELECTION UNIT SIGNAL LINE OUTPUT UNIT	XVDD	GND	UNREQUIRED
• Y DRIVER SHIFT REGISTER LEVEL SHIFTER SCAN LINE OUTPUT UNIT	YVDD YGVDD YGVDD	GND YGVSS YGVSS	REQUIRED REQUIRED REQUIRED
• SRAM DRIVER SRAM CONTROL SIGNAL GENERATING UNIT SRAM INVERTER POWER SOURCE UNIT	YGVDD SVDD	YGVSS SVSS	REQUIRED REQUIRED

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FIG.5

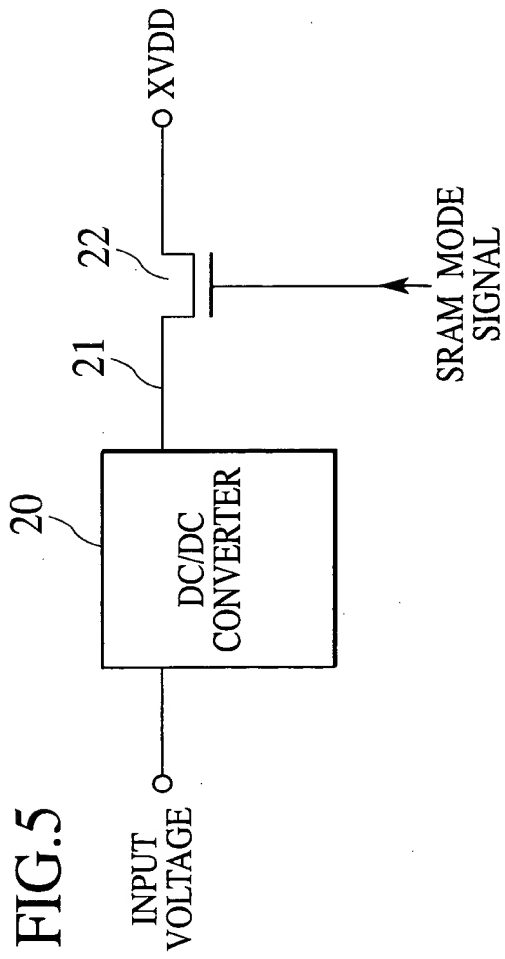


FIG.6

